Atty Docket No. 021230-000120US

PTO FAX NO.:

(703) 872-9306

ATTENTION:

Examiner Don P. Le

TELEPHONE NO.: (571) 272-1806 Group Art Unit 2819

## OFFICIAL COMMUNICATION

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FOR THE PERSONAL ATTENTION OF

**EXAMINER Don P. Le** 

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following document(s) in re Application of Malachy Devlin and Allan J. Cantle, Application No. 10/708,753, filed March 23, 2004 for PROGRAMMABLE POWER SUPPLY SYSTEM is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

## Document(s) Attached

- 1. Information Disclosure Statement attaching PTO/SB/08B
- 2. Transmittal Form

Number of pages being transmitted, including this page: 5

Dated: March 30, 2004

PLEASE CONFIRM RECEIPT OF THIS PAPER BY RETURN FACSIMILE AT (415) 576-0300

TOWNSEND and TOWNSEND and CREW LLP Two Embarcadero Center, Eighth Floor San Francisco, CA 94111-3834 Telephone: 650-326-2400

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PTO/SB/21 (08-03)

		Applica	ation Number	10/708,75	33			
TRANSMITTAL FORM			Filing Date		March 23, 2004			
			First Named Inventor Mal		Devlin			
(to be used for all correspondence after initial filing)			Art Unit		2819			
			Examiner Name		Don P. Le			
Total Number of Pages in This Submission	4	Attorne	ey Docket Number	021230-0	00120US			
	ENCLOSURES (Check all that apply)							
Fee Transmittal Form	☐ Drawlr	g(s)		After A	Allowance Communication to Group			
Fee Attached	Licens	ing-relate	d Papers		al Communication to Board of Appeals I terferences			
Amendment/Reply Petition				Appes Notice	al Communication to Group (Appeal , Brief, Reply Brief)			
			n to Convert to a ional Application		etary Information			
			ey, Revocation espondence Address	☐ Status	s Letter			
Extension of Time Request			minal Disclaimer		Enclosure(s) e identify below):			
Express Abandonment Request			est for Refund Number of CD(s)					
Information Disclosure Statement (attaching PTO/SB/08B)								
Certified Copy of Priority Document(s) Rems		The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.						
Response to Missing Parts/ Incomplete Application		i.						
Response to Missing Parts under 37 CFR 1.52 or 1.53								
			ICANT, ATTORNEY,	, OR AGEN	T			
Firm Townsend and T	ownsend a	ind Crew		N 20 626				
Individual Melvin D. Chan Reg. No. 39,626								
Signature (V COCK)								
Date March 30, 2004								
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I hereby certify that this correspondence is being factimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mall in an envelope addressed to: Commissioner for Patents, P.O. Box 1450. Alexandria, VA 22313-1450 on the date shown below.								
Typed or printed name Ralyn Lee								
Signature RVIVI	XXX			Date	March 30, 2004			

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAR 3 0 2004

In re application of:

MALACHY DEVLIN, et al.

Application No.: 10/708,753

Filed: March 23, 2004

For: PROGRAMMABLE POWER

SUPPLY SYSTEM

Examiner: Don P. Le

Art Unit: 2819

INFORMATION DISCLOSURE

STATEMENT UNDER 37 CFR §1.97 and

§1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. In accordance with 37 C.F.R.§1.98(d), copies of the references listed in this information disclosure statement are not being supplied to the patent office because they were previously submitted in parent U.S. patent application no. 10/076,883, filed 02/13/2002. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

MAR.30.2004 3:51PM TTC-PA 650-326-2422 NO.738 P.4/5

MALACHY DEVLIN, et al. Application No.: 10/708,753

**MS** sent 03/30/2004 Page 2

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Applicant believes that <u>no fee is required</u> for submission of this statement.

However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

Melyin D. Chan Reg. No. 39,626

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PTO/SB/08B (08-03)

Substitute for form 1449B/PTO		Complete if Known			
			0.00005	Application Number	10/708,753
INFORMATION DISCLOSURE				Filing Date	March 23, 2004
STAT	STATEMENT BY APPLICANT			First Named Inventor	Malachy Devlin
				Art Unit	2819
(use as many sheets as necessary)		Examiner Name	Don P. Le		
Sheet	1	of	1	Attorney Docket Number	021230-000120US

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), votume-issue number(s), publisher, city and/or country where published.	T i
	A	*EL7556BC Integrated Adjustable 6 Amp Synchronous Switcher," Elantec Semiconductor, Inc. data sheet, August 30, 2000, pp. 1-13.	
	В	Devlin, Malachy, et al., "DIME - The first module standard for FPGA based High Performance Computing," Proceedings of FPL'99, Glasgow, UK Sept. 1999, published as Springer LNCS 1673, 6 pages,	
	c	*MAX4598 Low-Voltage, Combination Single-Ended 8-to-1/Differential 4-to-1 Multiplexer,* Maxim Integrated Products data sheet, October 1998, pp. 1-12.	
	D	*DIME Module, Physical Level 0 Specification - Issue 4 NT301-0001,* Natistach Ltd. data sheet, November 23, 2000, pp. 1-21.	
	E	*Distributed DSP Processing, Level 1 Implementation of the DIME Module Standard NT301-0002," Nallatech Ltd. data sheet, April 22, 1999, pp. 1-13.	
	F	"Virtex™ -E. 1.8 V Field Programmable Gate Arrays," Xilinx DS022 (v 1.2) Advance Product Specification, January 28, 2000, pp. 1-190.	
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Examiner	Date	
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